Figure 2

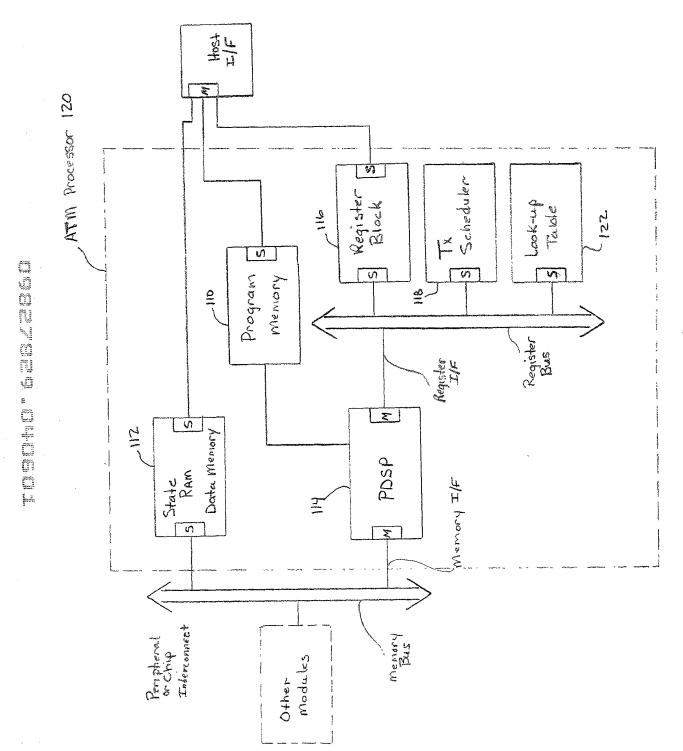
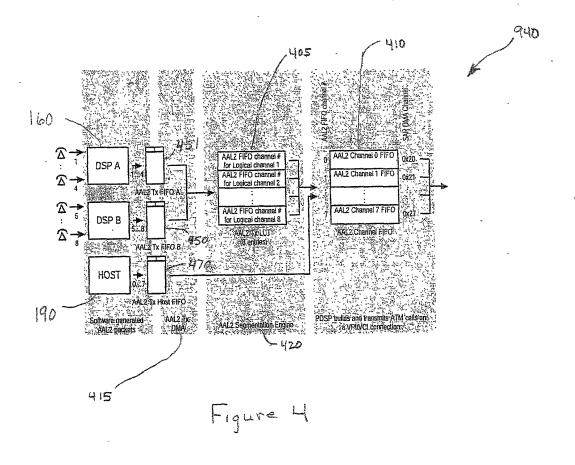


Figure 3



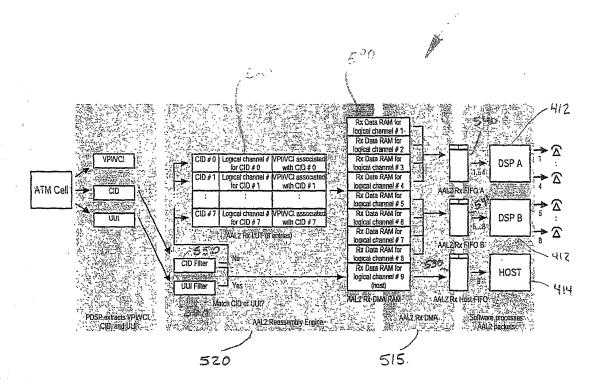


Figure 5

## 56-byte Aligned ATM cell

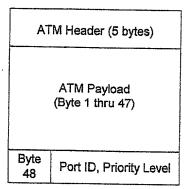


Figure 7

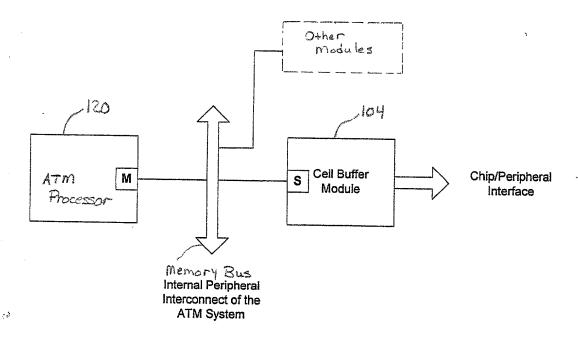


Figure 6 A

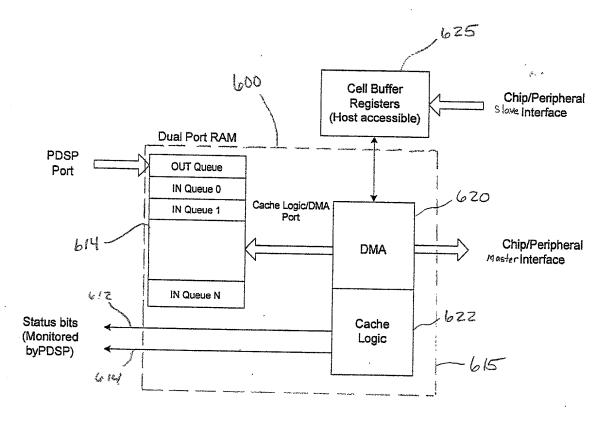


Figure 6B

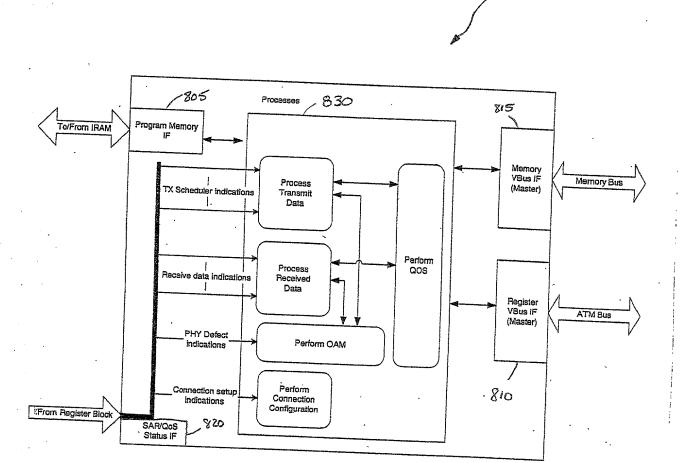


Figure 8